Hash loop

vR2 = vAdd4(M23,398ECE2D) ; = 0033F698

vR7 = 004076D4

vR15 = [vR7]

M24 = vAdd4(vR15,FFFFFFFF)

vR8 = vNand4(M24,M24)

vR15 = bawap(vR8)

vR9 = vShld(vR15,vR15,9)

vR4 = vNand4(vR9,vR9)

M25 = vNand4(vR4,vR4)

vR3 = Flag of vAdd4(M25,FFFFFFFF)

M26 = vAdd4(M25,FFFFFFFF)

vR8 = Flag of vNand4(M26,M26)

vR3 = 293

M27 = vNand4(vR3,vR3)

M28 = vNand4(M27,FFFFF7EA) ; = 11

vR8 = 202

M29 = vNand(vR8,vR8)

M30 = vNand4(M29,815)

vR10 = vAdd4(M30,M28) ; = 213

M31 = vNand4(vR10,vR10)

M32 = vNand4(M31,FFFFFFBF)

M33 = vShr4(M32,4)

JAdr = vAdd4 (M33,RootAddr) ; jcc condition M33 = 0 or 4

M34 = vNand4(vNand4(JAdr,JAdr),6E83B52D)

M35 = vNand4(JAdr,913CC483)

vR9 = JAddr = vNand4(M34,M35)

M36 = vAdd4(vR2,398ECE2D)

push vR9

jmp

vR4 = vAdd4(M36,398ECE2D)

vR1 = 000108A6

vR2 = vAdd4(0040000+vR1)

vR7 = 004076D4

M37 = vAdd4(vR7,4)

vR8 = [M37]

vR6 = vNand4(vR8,vR8)

vR8 = vAdd4(vR6,FFFFFFFF)

M38 = vAdd4(vR8,FFFFFFFF)

vR6 = vNand4(M38,M38)

vR8 = vShld(vR6,vR6,15)

M39 = vAdd4(vR8,FFFFFFFF)

vR12 = vNand4(M39,M39)

push vR12

push vR2

Hash = vCheck(addr,size)

vR13 = vNand4(Hash,Hash)

vR6 = vAdd4(FFFFFFFF,vR13)

M1 = vAdd4(FFFFFFFF,vR6)

vR8 = vNand4(M1,M1)

vR6 = vAdd4(FFFFFFFF,vR8)

M2 = vAdd4(vR6,FFFFFFFF)

vR8 = vNand4(M2,M2)

vR6 = vAdd4(vR8,FFFFFFFF)

vR8 = vNand(vR6,vR6)

vR13 = vAdd4(vR8,1D9C2648)

vR7 = 004076C8 Assignment before vCheck opcode

M3 = vAdd4(vR7,8)

M3 = [M3]

M4 = vNand(vR13,vR13)

vR10 = Flag of vAdd4(M3,M4)

M5 = vAdd4(vR10,vR10) ; M5 = FFFFFFFF

vR5 = Flag of vNand4(M5,M5)

M6 = vNand4(vR10,vR10)

M7 = vNand4(M6,FFFFF7EA) ; = 4

M8 = vNand4(vR5,vR5)

M9 = vNand4(M8,815) ; = 242

vR11 = vAdd4(M9,M7) ; = 246

M10 = vNand4(vR11,FFFFFFBF)

vR13BL = vShr4(M10,6)

vR9 = AEE9A100 Assignment before vCheck opcode

M11 = vNand1(vR13BL,vR9BL)

vR9BL = vNand1(M11,M11)

vR7 = 004076C8 Assignment before vCheck opcode

vR10 = Flag of vAdd4(vR7,C)

vR1 = vAdd4(vR7,C)

vR15 = 2A Assignment before vCheck opcode 循环次数

vR6 = Flag of vAdd4(vR15,FFFFFFFF)

vR11 = vAdd4(vR15,FFFFFFFF)

M12 = vNand4(vR10,vR10)

M13 = vNand4(M12,FFFFFFFE) ; = 0

M14 = vNand4(vR6,vR6)

M15 = vNand4(M14,1)

vR15 = vAdd4(M15,M13)

M16 = vNand4(vR15,vR15)

M17 = vNand4(M16,FFFFFFEF) ; = 10

M18 = vNand4(vR15,10)

vR10 = vNand4(M18,M17)

M19 = vNand4(vR10,FFFFFFBF)

M20 = vShr4(M19,4)

JAdr = vAdd4(M20, RootAddr) ; jcc condition M20 = 0 or 4

M21 = vNand4(vNand4(JAdr,JAdr),6EC33B7C)

M22 = vNand4(vR15,913CC483)

vR8 = vNand4(M21,M22) ; vR8 = JAddr

M23 = vAdd4(vR4,0C67131D3)